

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-535001	Application No. 09/989,483
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant John A. Wishneusky	
		Filing Date November 19, 2001	Group Art Unit 2182

(37 CFR §1.98(b))

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
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							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
CSK	AQ	Li, Yamin, et al., "The Effects of STEF in Finely Parallel Multithread Processors", <i>IEEE International Symposium on High-Performance Computer Architecture</i> , pp. 318-325, January 1995.
CSK	AR	Loikkanen, et al., "A Fine-Grain Multithreading Superscalar Architecture", <i>IEEE Comp. Soc.</i> , pp.163-168, October 1996.
CSK	AS	Thistle, et al., "A Processor Architecture for Horizon", <i>IEEE Proceedings Supercomputing</i> , pp. 35-41, November 1988.
	AT	

Examiner Signature <i>Ken S. Kim</i>	Date Considered 8-20-04
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Disclosure Form (PTO-1449)

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